

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** ( Not for submission under 37 CFR 1.99)

Application Number	10542327
Filing Date	2005-07-13
First Named Inventor	Tadashi MINOTANI et al.
Art Unit	2816
Examiner Name	
Attorney Docket Number	44471/317873

## **U.S. PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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/P.H./	1	BEHZAD RAZAVI, "A Design and Application of An Analog CMOS Integration Circuit," Publisher: Maruzen Co., Ltd., March 2003, pp. 686-688.	<input checked="" type="checkbox"/>
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- ☒ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

**SIGNATURE**

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Brenda O. Holmes 6799/	Date (YYYY-MM-DD)	2005-12-06
Name/Print	Brenda O. Holmes	Registration Number	40339

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